

Please amend claims 11, 17, 21, 23, 26-28, 30, 32, 33 and 34 as follows:

11. (Amended) A method for generating a switching frequency in a power conversion system, comprising:

generating a primary current;

a1 cycling one or more secondary current sources to generate a secondary current which varies over time; and

[supplying the primary and secondary currents]combining the secondary current with the primary current to be received at a control input of an oscillator for generating a switching frequency which is varied over time.

17. (Amended) A method for generating a switching frequency in a power conversion system, comprising:

a2 generating a primary voltage;

cycling one or more secondary voltage sources to generate a secondary voltage which varies over time; and

[supplying the primary and secondary voltages]combining the secondary voltage with the primary voltage to be received at a control input of a voltage-controlled oscillator for generating a switching frequency which is varied over time.

21. (Amended) A frequency jittering circuit for varying a power supply switching frequency, comprising:

a3 an oscillator for generating a signal having a switching frequency, the oscillator having a control input for varying the switching frequency; and

means coupled to the control input for varying the switching frequency, including:

one or more current sources coupled to the control input; and

a counter coupled to the output of the oscillator and to the one or more current sources.

²²23. (Amended) The circuit of claim [22]²¹ wherein the oscillator further comprises:

a primary current source coupled to the control input; and
a differential switch coupled to the primary current source.

²⁷26. (Amended) The circuit of claim [22]²¹ further comprising a transistor coupled to each current source and to the counter.

²⁵27. (Amended) The circuit of claim [22]²² wherein the primary current source generates a current I and each of [the]said one or more current sources generates a current lower than I.

²⁶28. (Amended) The circuit of claim [22]²² wherein the primary current source generates a current I and each of [the]said one or more current sources generates a second current lower than the current I, further comprising a transistor coupled to each current source connected to the counter.

²⁸30. (Amended) The circuit of claim [22]²¹ wherein the oscillator further comprises:
a primary voltage source coupled to the control input; and a differential switch coupled to the primary voltage source.

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~~30~~32. (Amended) The circuit of claim ~~31~~ further comprising:

one or more comparators coupled to the capacitor; and

[the] means coupled to the capacitor for alternately charging and discharging the capacitor.

³¹
~~33~~33. (Amended) A power supply having a transformer coupled to an input voltage, the transformer having a primary winding, the power supply comprising:

an oscillator for generating a signal having a frequency, the oscillator having a control input for varying the frequency of the signal, the oscillator including:

a primary current source coupled to the control input;

a differential switch coupled to the primary current source;

a capacitor coupled to the differential switch; and

a comparator coupled to the differential switch;

a digital to analog converter coupled to the control input, the [analog to] digital to analog converter having one or more current sources, wherein the primary current source generates a current I and each of [the]said one or more current sources generates a current lower than I;

a counter coupled to the output of the oscillator and to the current sources of the digital to analog converter; and

a power transistor coupled to the oscillator and to one terminal of the primary winding, the power transistor modulating its output in providing a regulated power supply output.

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~~34~~34. (Amended) A power supply having a transformer coupled to an input voltage, the transformer having a primary winding, the power supply comprising: